

Application No. 10/717,284
Amendment dated December 22, 2005
After Final Office Action of October 13, 2005

Docket No.: 08211/0200252-US0 (P05722)

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REMARKS

Prior to entry of this paper, Claims 23, 25-37 and 41-47 were pending. Claims 23, 25-32, 34-37, 41, 43-45, and 47 are allowed. Claims 33, 42, and 46 were rejected. In this paper, Claims 33 and 42 are cancelled. Claims 23, 25-32, 34-37 and 41, and 43-47 are currently pending. For at least the following reasons, Applicants respectfully submit that each of the presently pending claims is in condition for allowance.

Allowed Claims (23, 25-32, 34-37, 41, 43-45, and 47)

Applicants appreciate the indication that claims 23, 25-32, 34-37, 41, 43-45, and 47 are allowed and thank the Examiner for his work on this matter.

Claim 46

The rejection to Claim 46 is respectfully traversed. It is respectfully submitted that Claim 46 is condition for allowance at least because Singer does not disclose "maintaining the substantially constant voltage difference between the control input voltage and the input voltage is not accomplished via capacitive sampling", as recited in Applicant's Claim 46. In FIG. 7 of Singer, capacitive sampling is used to provide a constant voltage between the control input voltage (at the gate of M1) and the input voltage V1. Capacitor C_{boot} samples the voltage (V₊₊ - V₋₋) during interval 1. During interval 2, capacitor C_{boot} is placed across the gate-to-source junction of transistor M1 so that the voltage at node N3 is equal to (V₊₊ - V₋₋) + V_{in} during interval 2. Accordingly, capacitive sampling is used to maintain a constant voltage between the control input voltage and the input voltage. As stated in Col. 11, lines 33-41 of Singer, "the difference between the positive supply voltage V₊₊ and the negative supply voltage V₋₋ is sampled onto capacitor C_{BOOT} at the end of Interval 1 and that voltage is boosted by the input voltage V_{in} during Interval 2 and provided as the gate drive voltage of transistor M1, for maintaining a constant "on" resistance R_{on} of transistor M1, independent of the instantaneous voltage of V_{in}, and regardless of whether V_{in} changes between intervals."

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CONCLUSION

It is respectfully submitted that each of the presently pending claims (Claims 23, 25-32, 34-37 and 41, and 43-47) are in condition for allowance and notification to that effect is requested. Examiner is invited to contact the Applicants' representative at the below-listed telephone number if it is believed that the prosecution of this application may be assisted thereby. Although only certain arguments regarding patentability are set forth herein, there may be other arguments and reasons why the claimed invention is patentable. Applicant reserves the right to raise these arguments in the future.

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Respectfully submitted,

By 

Matthew M. Gaffney

Registration No.: 46,717

DARBY & DARBY P.C.

P.O. Box 5257

New York, New York 10150-5257

(206) 262-8900

(212) 527-7701 (Fax)

Attorneys/Agents For Applicant

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